

Coordinating Synchronous (Real-Time) Motion Between EPICS Systems and PMAC Controllers

Curtis S. Wilson
Delta Tau Data Systems, Inc.

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The Path Until Now

- History of different controllers from multiple vendors
- Relatively unplanned evolution
- Result: “lowest common denominator” approach to EPICS/controller interface
- Usually single-axis, single-destination (point-to-point) move
- Extensions are difficult to achieve in current framework (e.g. streaming points in real time)



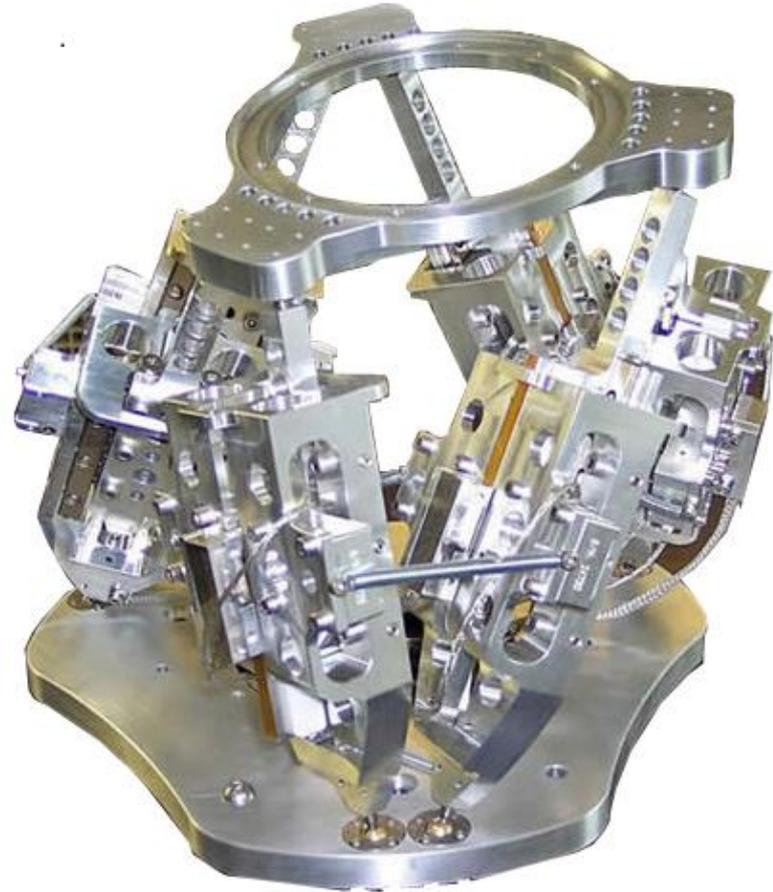
Multi-Axis & Multi-Move Sequences

- Multiple axes can be grouped together into “coordinate systems”
 - Easy to command fully synchronized motion of all axes in C.S.
 - Can be abstracted to look like single-axis command
- Coordinate system can execute multi-move sequences of one or more axes
 - Sequence of pre-planned moves executed in order
 - Can be abstracted to look like single-move command



Kinematics Subroutines

- Permit specification of motion in “work” (tool-tip) coordinates, even with very different underlying actuator geometries
- Permit mathematically non-linear mapping between tool-tip and actuator coordinates
- Inverse kinematics (tip -> actuator) solved at small intervals along paths
- Kinematic subroutines can be written in Script or C



Tightly Coupling Motion and Measurements

- Accurately tying digital I/O to physical position
 - “Capturing” position on digital input
 - “Comparing” position to create digital output
- Doing this while in high-speed motion
 - Accuracy is mostly product of velocity and time uncertainty
 - Great increase in throughput for given accuracy (or accuracy for given throughput) if time uncertainty can be reduced
- Increasing (effective) position measurement resolution helps accuracy

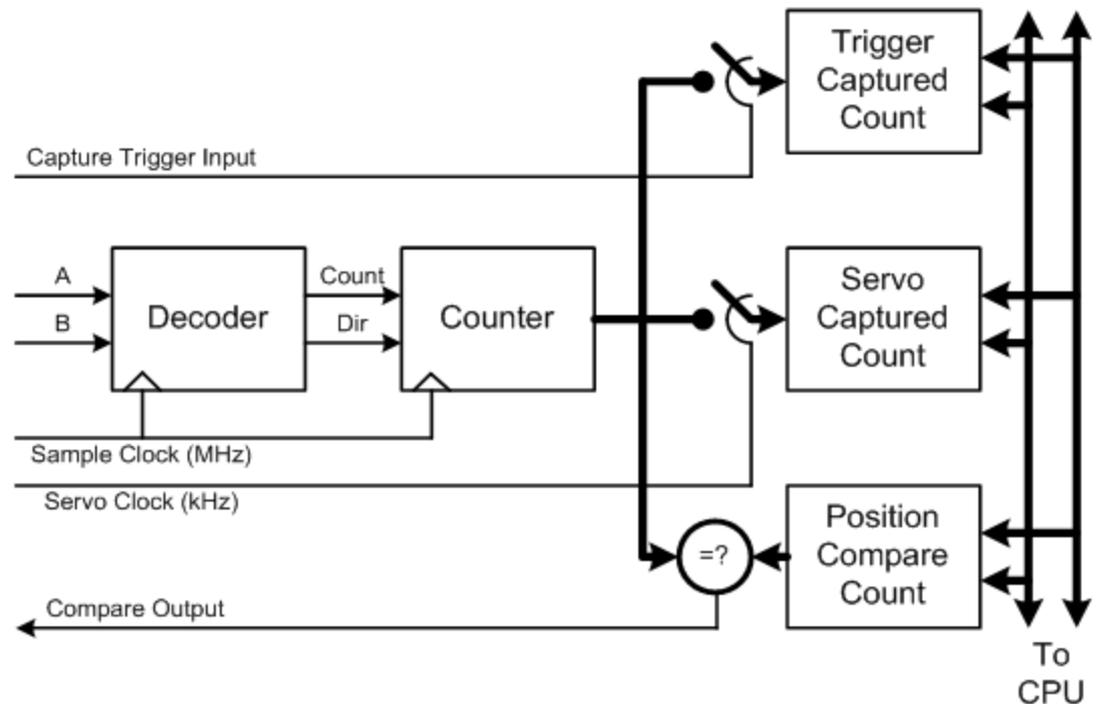
Basic Hardware Capture & Compare

At high (MHz) frequencies:

- Encoder inputs are sampled
- Counter can be incremented
- Present count can be latched on trigger
- Present count is checked against “compare” value

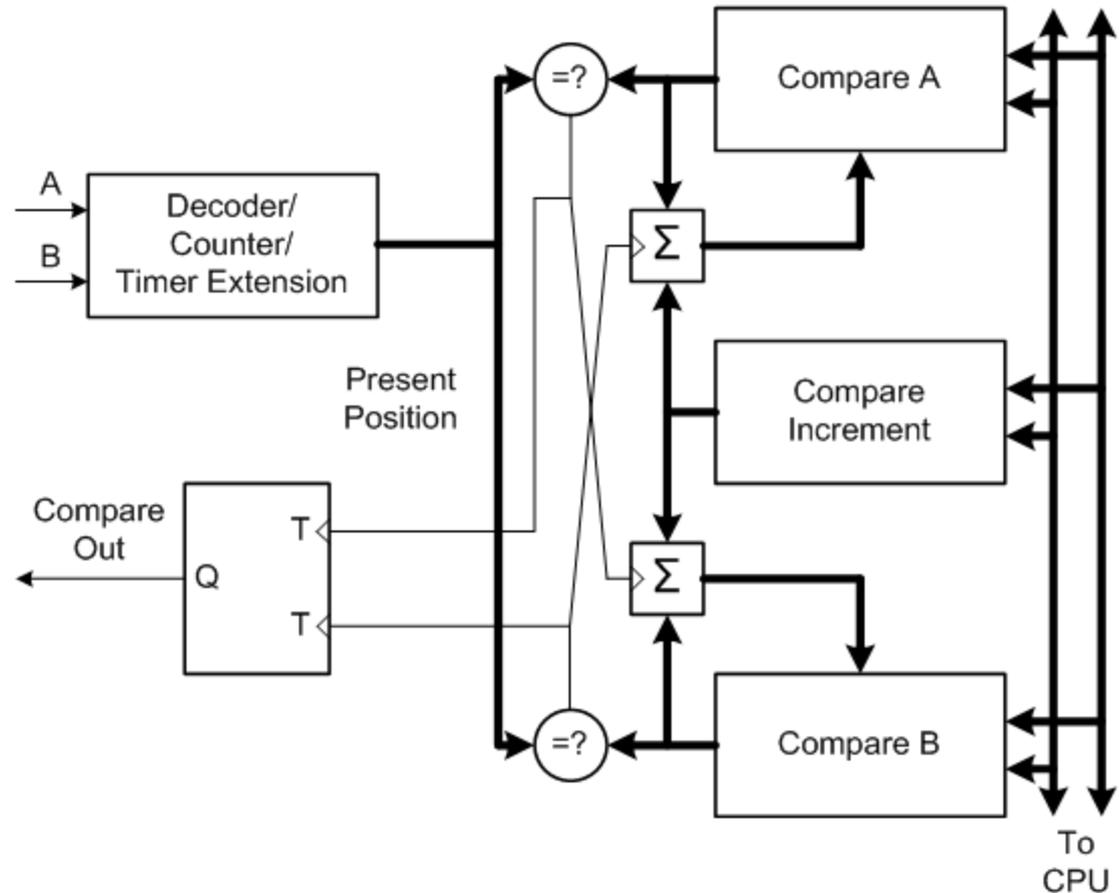
At low (kHz) frequencies:

- Counter is latched for servo use



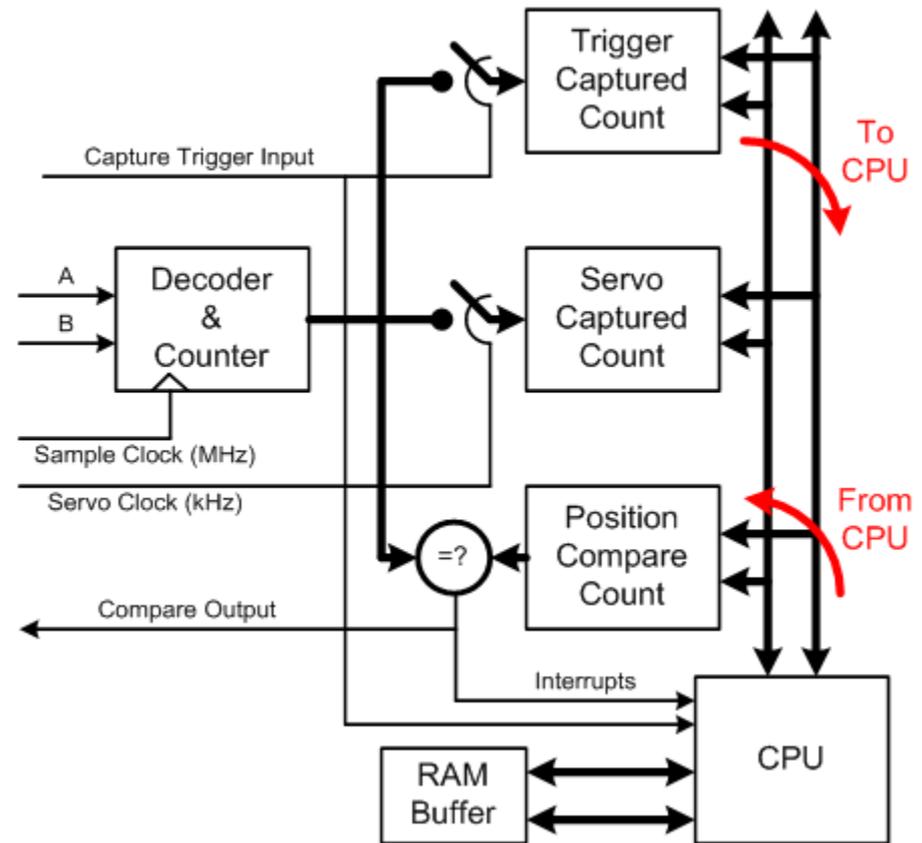
Compare Hardware Incrementing

- Two compare registers for a counter
- Each can toggle digital output
- Toggle signal also causes increment value to be added to other compare value
- Uniformly spaced pulse train can be generated at very high frequency



Capture/Compare Interrupt Update

- Capture and compare events can interrupt CPU at highest priority
- Custom ISR can react quickly to prepare for next event
- Typically store captured position to RAM buffer
- Typically load next compare position from RAM buffer
- Updates to 75 kHz
- Keep ISR short!

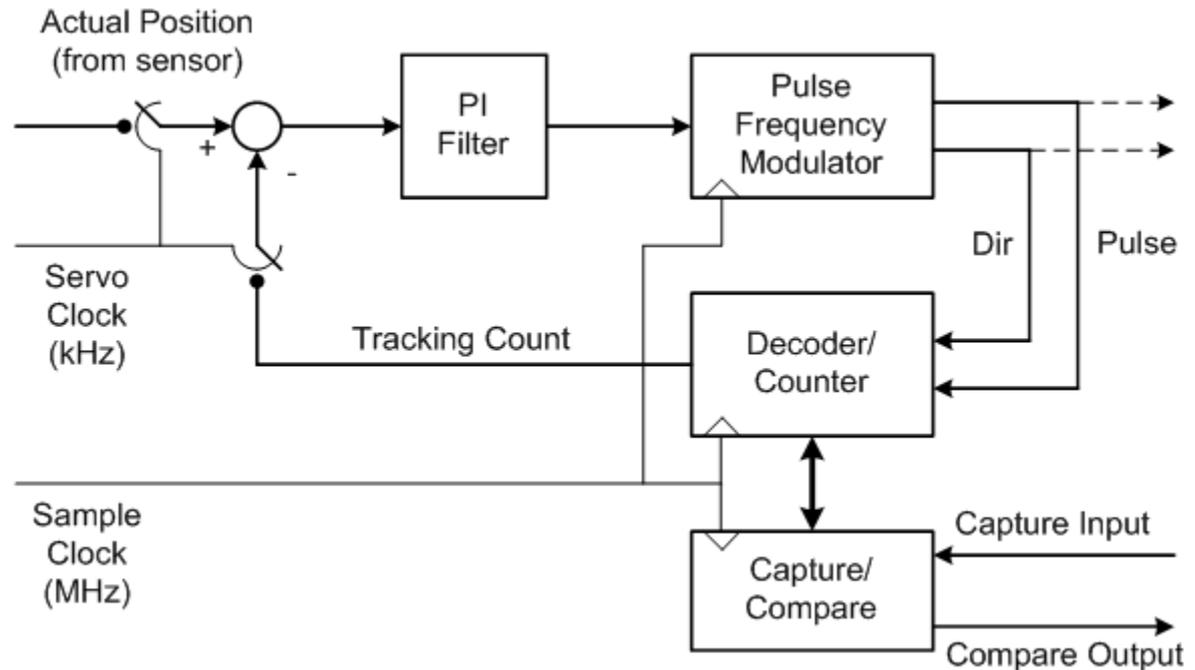


Sensors with Low-Frequency Access

- Many sensors do not support access at high enough frequency (or asynchronous access) for capture/compare purposes
 - Parallel-data interferometers
 - Serial-data encoders
 - SAR analog/digital converters
- Typically accessed at (kHz) servo rates
- To improve capture/compare accuracy, must tie to high-frequency (MHz) circuitry somehow
- Several methods can be used

Tracking Loop with Simulated Encoder

- Tracking loop reads sensor position once per servo cycle, compares it to counter value
- Error drives PFM circuit that can generate pulses at MHz rates
- Pulses feed counter
- Capture and compare use counter as for real encoder



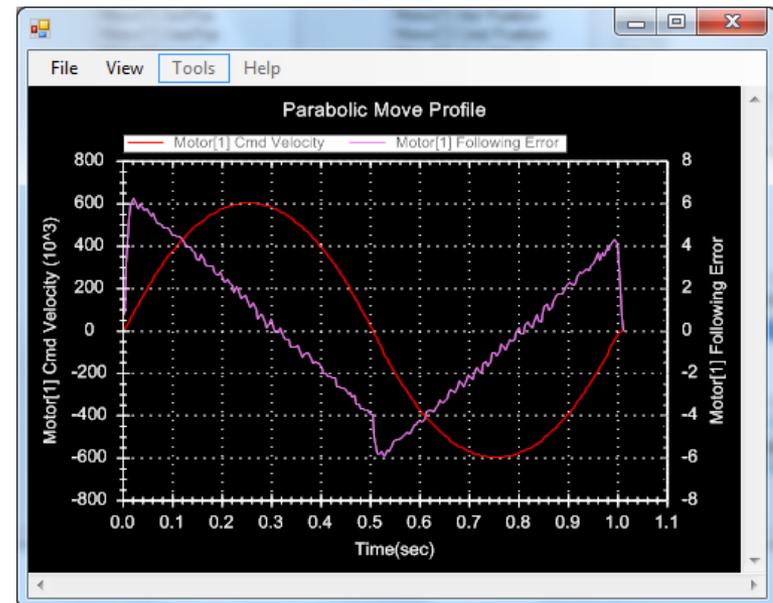
Uses of Virtual Motors

- Without physical actuator attached, have great flexibility in commanding
 - Software functions for calculating and executing move commands
 - Can use interface circuitry or not
- Under EPICS single-motor command, can act as the commanded motor
 - Executes single point-to-point move
 - Real motors can do more sophisticated sequence
 - Full coordination of real and virtual motors
- With simulated encoder circuitry, can also tie to physical I/O



Synchronous Data Gathering

- Synchronous data logging at up to servo frequency
- Up to 128 memory and/or hardware registers per sample
- Key hardware registers latched on clock edges with jitter in low nanoseconds
- Capability for continuous streaming of gathered data
 - Can create real-time “scope” plot
- Easy export (in CSV format) to analysis programs



Full Embedded Computer Functionality

- Power PMAC is an embedded computer with built-in machine control application
- Provides full file system and communications tool suite
- Supports user C functions
 - Custom phase and servo algorithms
 - Fast kinematics and “PLC” functionality
- Supports independent C applications
 - Can eliminate separate computer
 - Reduces communications needs
 - e.g. EPICS software

